

Translation

PATENT COOPERATION TREATY

PCT

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

(Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

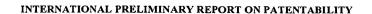
Applicant's or agent's file reference JST-107-PCT		FOR FURTHER ACTION	N	See Form PCT/IPEA/416			
International application No.		International filing date (day)	/month/year)	Priority date (day/month/year)			
PCT/JP2004/002889		05.03.2004		17.03.2003			
	International Patent Classification (IPC) or national classification and IPC						
and the state of t							
Applicant							
JAPAN SCIE	NCE AND TE	CHNOLOGY AGENO	CY .				
1. This report is tunder Article 3	the international prelin 5 and transmitted to th	unary examination report, es applicant according to Artic	tablished by this Ir. le 36.	nternational Preliminary Examining Authority			
2. This REPORT	consists of a total of	4	sheets, including	this cover sheet.			
3. This report is a	lso accompanied by Al	NNEXES, comprising:					
a 🖂 (see	nt to the applicant and	to the International Bureau) a	total of 9	shoots on follows:			
		•	-	sheets, as follows:			
	sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications authorized by this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions).						
	sheets which supers	ede earlier sheets, but which t	this Authority consi	iders contain an amendment that goes beyond			
	the disclosure in the Box.	international application as i	filed, as indicated i	in item 4 of Box No. I and the Supplemental			
b. ☐ <i>(se</i>)							
,		on our only a tour or (mateu	ie type and number	or electronic carrier(s))			
relate	, containing a sequence listing and/or tables related thereto, in computer readable form only, as indicated in the Supplemental Box Relating to Sequence Listing (see						
Section	on 802 of the Administ	rative Instructions).	ated in the Supplen	nental Box Relating to Sequence Listing (see			
4. This report cor	ntains indications relati	ng to the following items:					
Box N	o. I Basis of the	report					
Box N	o. II Priority						
Box N	o. III Non-establi	shment of opinion with regard	I to novelty, inventi	ve step and industrial applicability			
Box N	o. IV Lack of unit	v of invention					
	Box No. IV Lack of unity of invention Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability;						
	citations and explanations supporting such statement						
Box No. VI Certain documents cited							
Box N	o. VII Certain defe	cts in the international applica	ation				
Box No. VIII Certain observations on the international application							
Date of submission of the demand Date of completion of this report			s report				
			-	-			
Name and mailing address of the IPEA/JP			rized officer				
Facsimile No.			hone No				



INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.
PCT/JP2004/002889

Box	No. I	Basis of the report		
1.	With rega	rd to the language, this report is based on the internation under this item.	nal application in the language in	which it was filed, unless otherwise
	This which	s report is based on translations from the original langua ch is the language of a translation furnished for the purp international search (Rule 12.3 and 23.1(b)) publication of the international application (Rule 12.4 international preliminary examination (Rule 55.2 and/	oses of:	,
2.	this repor	international application as originally filed/furnished description:	report is based on (replacement si e referred to in this report as "or	iginally filed" and are not annexed to
	page		received by this Authority on	as originally filed/furnished
	page			F002.21.22
	\square	claims:	_	
	nos.			as originally filed/furnished
	nos.			with any statement) under Article 19
	nos.			•
	nos.			
	the o	drawings:	-	
	shee	- ets 1-8		as originally filed/furnished
	shee	ets*	received by this Authority on	<u> </u>
	shee	ets*	received by this Authority on	
	a se	quence listing and/or any related table(s) – see Supplem	-	
3.	The	amendments have resulted in the cancellation of:		
		the description, pages		
	\boxtimes	the claims, nos. 2,8-11		
		the drawings, sheets/figs		
		any table(s) related to sequence listing (specify):		
4.	This they	s report has been established as if (some of) the amend y have been considered to go beyond the disclosure as fil	ments annexed to this report and led, as indicated in the Supplemen	listed below had not been made, since tal Box (Rule 70.2(c)).
		the description, pages		
		41 1 1		
		the drawings, sheets/figs		
		the sequence listing (specify):		
		omy toble(a) related to reserve that a constant		
*	If item 4 a	applies, some or all of those sheets may be marked "sup	erseded."	



International application No. PCT/JP2004/002889

Box	No. V Reasoned statemer citations and expla	Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement		
1.	Statement			•
	Novelty (N)	Claims		YES
		Claims	1, 3-7	NO
	Inventive step (IS)	Claims		YES
ı		Claims	1, 3-7	NO
	Industrial applicability (IA)	Claims	1, 3-7	YES
		Claims		NO

2. Citations and explanations (Rule 70.7)

Claims 1 and 3 to 5

A semiconductor storage element comprising a lower electrode formed on a semiconductor single crystal substrate with an insulating layer interposed therebetween, a ferroelectric film and an upper electrode is known (see JP 2002-299582 A, JP 2002-299581 A and JP 2002-110932 A, for example), and it would be easy for a person skilled in the art to apply the configuration set forth in document 1 (JP 9-089651 A) cited in the international search report as a configuration for the lower electrode, ferroelectric film and upper electrode in such a known semiconductor storage element. Therefore the inventions set forth in claims 1 and 3 to 5 lack novelty and do not involve an inventive step.

Claims 6 and 7

An ultrasonic sensor comprising a lower electrode formed on a semiconductor single crystal substrate with an insulating layer interposed therebetween, a ferroelectric film and an upper electrode is known (see JP 7-298394 A, JP 7-183397 A and JP 7-055919 A, for example), and it would be easy for a person skilled in the art to apply the configuration set forth in document

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International application No.
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	101/012001/002009			
Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement				
1 (JP 9-089651 A) as a configuration for the lower				
electrode, ferroelectric film a	and upper electrode in such			
a known semiconductor storage e	element. Therefore the			
invention set forth in claims (and 7 lacks novelty and			
does not involve an inventive s	step.			
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